

талонн е проект

писание

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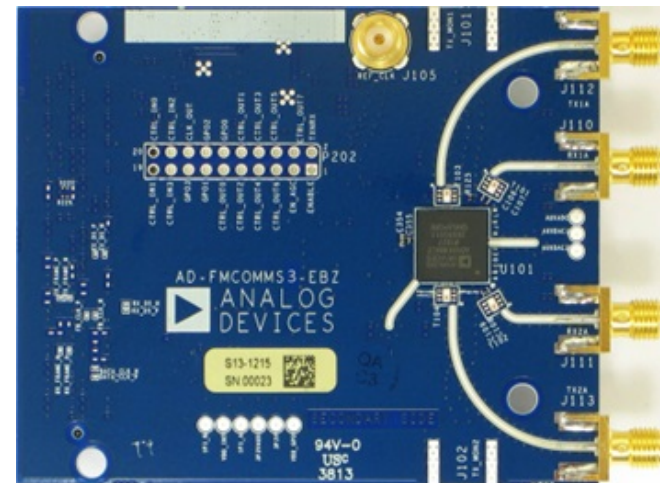
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Reference Designs

Analog Devices has developed hundreds of reference designs to help solve our customers' system level application challenges from mixed signal designs, to software drivers and algorithm development.

Software Defined Radio

The AD-FMCOMMS3-EBZ is a high-speed analog module designed to showcase the AD9361, a high performance, highly integrated RF transceiver intended for use in RF applications.



Explore Circuits from the Lab

Circuits from the Lab are built and tested by ADI's applications experts, offering comprehensive documentation, complete hardware and integration files, and factory tested evaluation hardware.



Drivers & Reference Code

Software repository for various levels of code development, including device drivers, HDL, and basic algorithms. [View More](#)

FPGA HDL Code

Linux Device Drivers

No-OS Drivers

FPGA and Processors Compatible Reference Designs

Analog Devices' makes it easier for customers to connect Analog Devices' high-speed and precision data converters, sensors, RF ICs and other components to FPGAs and microprocessors.

Circuits from the Lab

Circuits from the Lab® Reference Designs are commonly used as standalone solutions, or to build more complex circuits and subsystems.

Built and tested for function and performance by ADI's applications experts, they offer:

- Comprehensive documentation
- Complete design and integration files
- Factory-tested evaluation hardware
- Circuits from the Lab Reference Designs Disclaimer

Use Circuits from the Lab® in Your Next Design

Watch how two engineers use Analog Devices' Circuits from the Lab to help solve their design challenge. From test data to hardware evaluation, to project integration, we make it easy to save time while lowering the risk in your circuit design.

Introduction to Reference Circuits

Hardware Enables...

- Modular approach to system designs
- Application ready software
- Fast prototyping with FPGA and MCU

Design & Integration Files Support...

- Schematic, layout, BOM downloads

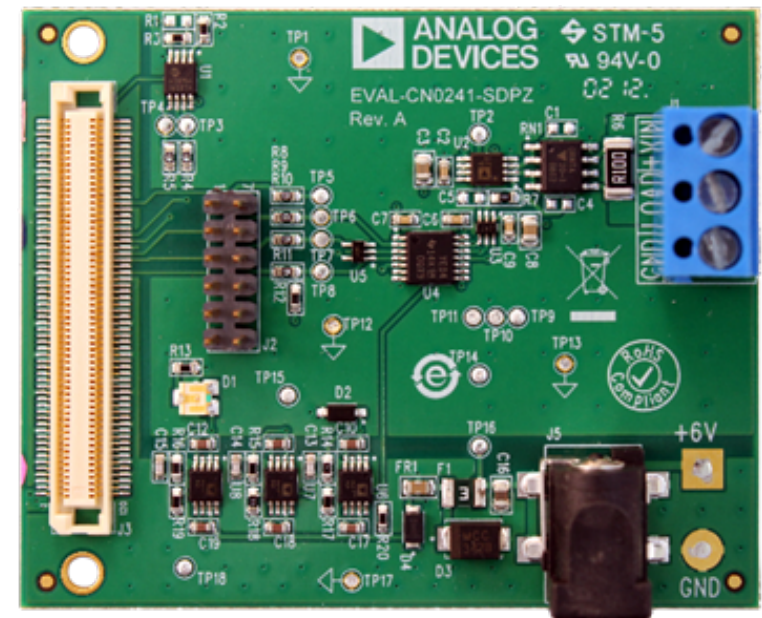
- Linux code for driver development
- Simplified application integration

Documentation Provides...

- Expert applications knowledge
- Understanding of circuit capabilities
- Tested and verified performance data

What makes Circuits from the Lab reference designs unique?

- Address analog, RF, and mixed-signal design challenges by applying ADI's vast applications expertise.
- Circuit designs are built and tested by the experts, to ensure both performance and function.
- Low cost hardware allows for evaluation and rapid prototyping with several development platforms.
- Thorough documentation and design files ease application understanding and minimize system integration issues.



Drivers & Reference Code

Analog Devices provides device drivers for both FPGA and microprocessor designs, which help facilitate software development for developers using digital devices from ADI.

FPGA HDL Code

HDL code and projects developed to work with FPGAs and our ADI mixed signal devices.

Linux Device Drivers

Analog Devices creates and maintains Linux Drivers for various Analog Devices products.

No-OS Drivers

The majority of ADI's products are peripherals to a non-ADI digital engine (FPGA, Microprocessor, or Microcontroller).

FPGA and Processors Compatible Reference Designs

Validated Processor Reference Designs

Analog Devices makes it easier for customers to connect high speed and precision data converters, sensors, power management, and other solutions to field programmable gate arrays (FPGAs), graphics processing units (GPUs), systems on a chip (SoCs), and microprocessors. These designs are supported to enable faster go-to-market strategies across embedded platforms including automotive, consumer, data center, healthcare, and industrial segments. Through collaboration with industry-leading processor partners, we aim to simplify system design by providing reference design solutions, tools, device drivers, and reference project examples for rapid prototyping and reduced development time.

FPGA Solutions



ADI has worked closely with Altera and Strategic Altera Partners to provide you with approved and tested solutions for your FPGA- and CPLD-based systems. Here you will find a host of useful tools that will allow you to select approved solutions for Altera.



ADI has worked closely with Xilinx and Strategic Xilinx Partners to develop proven solutions for Xilinx-based systems. Here you will find a host of useful tools that will facilitate your design efforts.

GPU/SoC



ADI has worked closely with NVIDIA to provide supported design solutions across its Jetson™ platform. ADI's extensive partnership with NVIDIA includes designs for automotive, consumer, data center, healthcare, and industrial segments. Please check out our dedicated Jetson page.

Microprocessors



ADI has worked closely with NXP (Freescale) and Strategic NXP (Freescale) Partners to provide you with approved and tested solutions for your processor-based systems. Here you will find a host of useful tools that will allow you to select approved solutions for NXP.

System on Chip (SoC) Processors



ADI has worked closely with Ambarella and Strategic Ambarella Partners to provide you with approved and tested solutions for your SoC processor-based systems. Here you will find a host of useful tools that will allow you to select approved solutions for Ambarella.

ADI has worked closely with Blaize and their partners to provide you with approved and tested solutions for your SoC processor-based



systems. Here you will find a host of useful tools that will allow you to select approved solutions for Blaize.

Analog Devices Partner Programs



The Partner programs at ADI offer an expansive list of collaborative partners seeking to assist in design projects across a wide range of markets and industries. Our partners' hardware and software, coupled with expert design services, will enable you to find the best solutions for your design needs and challenges.

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